

Fig. 1

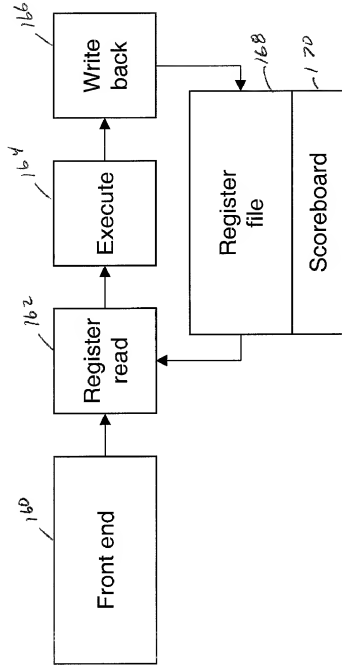


Fig. 1A

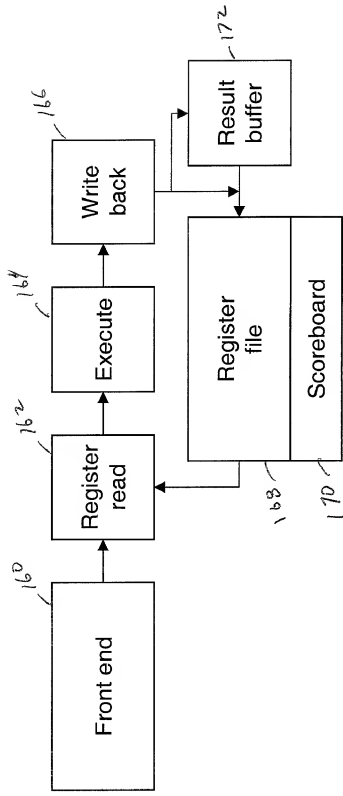


Fig. 1B

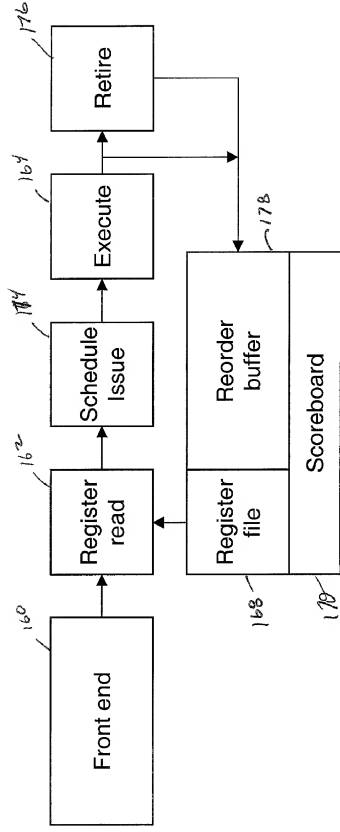


Fig. 1C

INST#	INSTRUCTION	DESCRIPTION
1:	mov 0 -> R1	# establishes R1
2:	ld (mem) -> R2	# loads R2 from memory
3:	cmp 0, R2 -> P1	# compares R2 to 0 and sets resulting predicate P1
4:	(P1) add 5, R1 -> R1	# adds 5 to R1 if P1 is true
5:	sub 10, R1 -> R3	# sets R3 with the difference of 10 and R1

Fig. 2

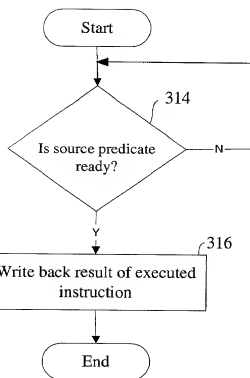


FIG. 3C

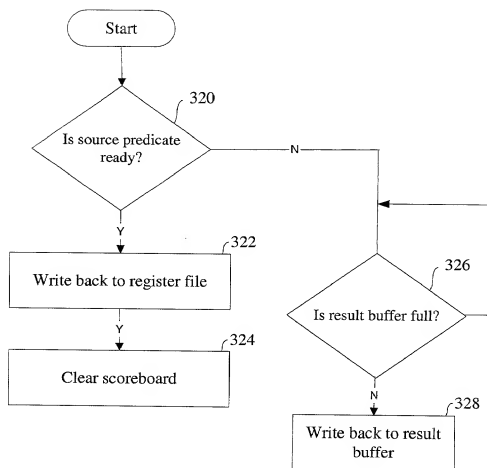


FIG. 3D

2000

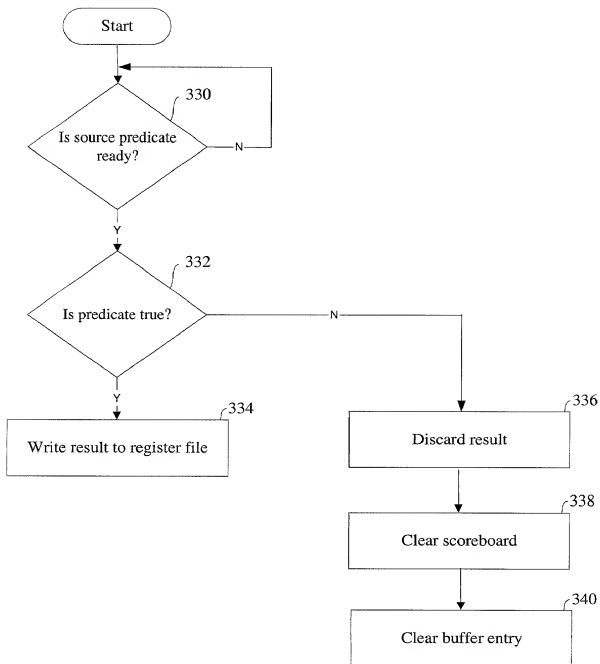


FIG. 3E

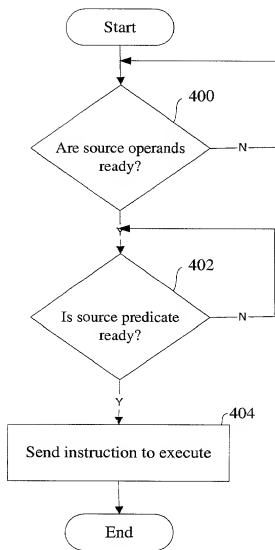


FIG. 4A

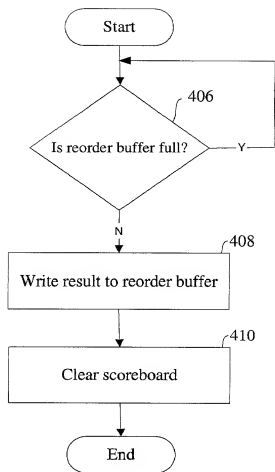
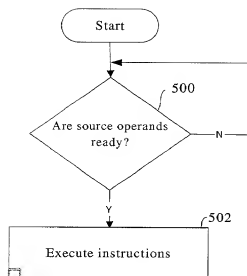
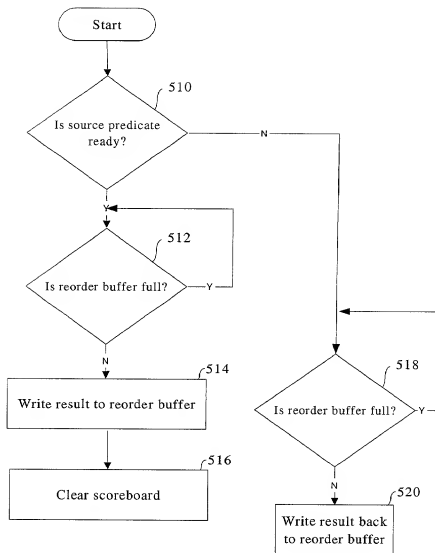


FIG. 4B

**FIG. 5A****FIG. 5B**

1

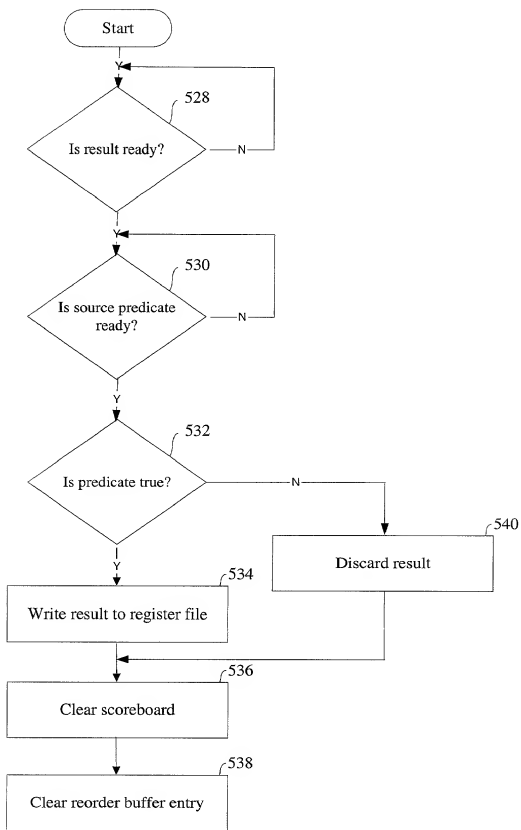


FIG. 5C